

Objection to the Drawings

The Examiner has objected to the drawings as being incomplete. The Examiner has stated that Pages 1 and 2 are missing from the application. Enclosed please find Pages 1 and 2 as requested by the Examiner. The copies of Pages 1 and 2 are being submitted for the convenience of the Examiner and to complete his file. Formal drawings will be filed upon receipt of a Notice of Allowance. In view of the attached drawings it is respectfully submitted that this objection is satisfied and should be withdrawn.

Rejection of Claims 1-17 under 35 U.S.C. 103(a)

Claims 1-17 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Kimura et al. in view of Owen et al.

The present claimed invention discloses a method and system for processing a digital data stream of MPEG coded image representative information. An MPEG compatible signal processing network includes an input network for receiving the datastream of MPEG compatible data and a decompressor for decompressing the compressed MPEG compatible data. A plurality of similar, concurrently operative compressors respectively recompress different datastreams derived from the decompressed data, and a memory stores the recompressed data.

Specifically, a plurality of decompressed MPEG datastreams are recompressed by plural recompressors before being stored in memory by the present claimed invention.

More specifically, claim 1 recites:

"In a system for processing a digital data stream of MPEG coded image representative information, an MPEG compatible signal processing network, comprising
an input network for receiving a datastream of MPEG compatible data;
a decompressor for decompressing said compressed MPEG compatible data to produce decompressed data;
a plurality of similar, concurrently operative compressors respectively recompress different datastreams

derived from the decompressed data to produce recompressed data; and
a memory for storing the recompressed data from said plurality of compressors."

The Examiner states that it would be obvious to combine the encoding of Owen et al. with the decoding circuit of Kimura et al. to produce the present claimed invention. Applicants respectfully disagree.

The decoding circuit of Kimura et al. restores an original image using a code string created by the image coding apparatus (see column 16, lines 2-4). The decoders retrieve the code string stored in the shared memory by the encoder circuits. The decoders only retrieve data from memory and do not store data into memory. Alternatively, the present claimed invention decompresses a received compressed MPEG compatible data and then recompresses different decompressed data streams in a plurality of compressors for storage into a memory. Thus, it is clear that the decoder of Kimura et al. performs the opposite operation of the present claimed invention.

Furthermore, Owen et al. includes a decoder for storing decoded data into memory. This is the opposite function of the decoder apparatus of Kimura et al. It is thus respectfully submitted that combination of the decoding apparatus of Kimura et al. (which retrieves data from a shared memory for restoring an image) with the circuit of Owen et al. which decodes data (for input to a memory) would not produce an operable device as they perform opposing functions. Thus, the attempted combination of Kimura et al. with Owen et al. is inappropriate.

It is thus respectfully submitted that Kimura et al. when taken alone or in combination with Owen et al. would not make the present invention as claimed in claim 1 unpatentable. Claims 10, 14 and 17 are all independent claims including limitations similar to those discussed above with respect to the rejection of claim 1. It is thus respectfully submitted that claims 10, 14 and 17 are also patentable over Kimura et al. when taken alone or in combination with Owen et al. As all remaining claims are dependent on claims 1, 10, 14 or 17 it is respectfully submitted that these claims are also patentable over Kimura et al. when taken alone or in combination with Owen et al.

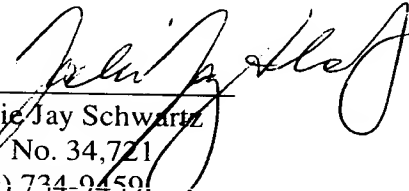
pixel data. Actually, Kimura et al. neither discloses nor suggests interleaved pixel data as claimed by the present invention. The passage of Kimura et al. referred to by the Examiner in the Office Action discloses a plurality of encoders each processing signals of a respective area. Nowhere in Kimura et al. is it disclosed or suggested that the signals of the different areas are interleaved as taught and claimed by applicant.

It is thus respectfully submitted that in view of the above remarks regarding the rejection of claim 3, that claim 3 is patentable over Kimura et al. when taken alone or in combination with Owen et al. It is further respectfully submitted that claims 4, 5, 6, 7, 13, 15, 16 and 17 are also patentable over Kimura et al. when taken alone or in combination with Owen et al. as they also contain limitations directed to the interleaving of data.

In view of the above remarks it is respectfully submitted that this rejection is satisfied and should be withdrawn.

No other fee is believed due. However, if an additional fee is due, please charge the fee to Deposit Account 07-0832.

Respectfully submitted
Kranawetter et al.

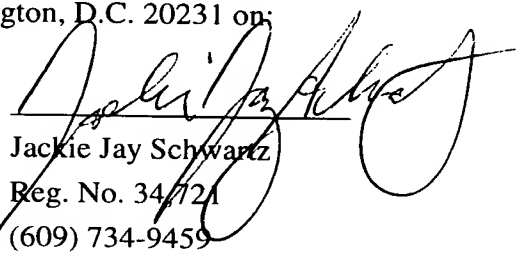
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I hereby certify that this amendment is being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to the Assistant Commissioner for Patents Washington, D.C. 20231 on:

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